The Design of a Fuel-Cell-Based Power-Quality Control Center to Realize Unbundled Power-Quality Supply

Y. Q. Zhan, Y. H. Li, S. S. Choi, S. Rajakaruna, and D. Mahinda Vilathgamuwa

Abstract—The application of a power-quality control center (PQCC) to provide an unbundled quality of electricity supply is examined. The center consists of back-to-back power converters, a dc bus and a solid-oxide fuel-cell distributed generator (SOFC/DG). The purpose is to use the flexibility offered by the converters and SOFC/DG to realize varied levels of ac supply quality to the loads. Through the concept of a feasible operating area of the SOFC/DG, analysis shows that the ability of the PQCC to accommodate instantaneous power demand change is constrained. This is due to limits imposed on the fuel utilization factor of the SOFC/DG. A feasible PQCC operating scheme is then proposed. It fully exploits the ability of the converters and the distributed generator to respond to external disturbances. In the event of a system voltage disturbance, the PQCC is to operate in one of two scenarios in accordance to the severity of the disturbance. Based on the dynamic model derived for the power network, a design procedure of the PQCC control system is also described. Simulation results show the efficacy of the proposed scheme.

Index Terms—Distributed generator (DG), fuel cell (FC), power-quality control center (PQCC), unbundled PQ supply, voltage sag.

I. INTRODUCTION

IN MANY parts of the world, the design and operation of power systems would have to be re-examined in the light of the deregulation of their utility industry. Introducing small-scale power generation on the customer side of distribution systems would become more viable as the new paradigm allows the distributed generators (DGs) easier access to the transmission-distribution facilities [1]. At the load end, meanwhile, the trend is toward the increasingly widespread use of computer-controlled and automated equipment. These loads require a high level of supply quality. Thus, it is envisaged that the installation of the DG that is electrically closer to these sensitive loads would also lead to improved supply quality and reliability. Unfortunately, the introduction of DG brings with it some technical challenges as well. For example, the DG can cause power flows along unintended paths under certain operational conditions. This could result in an undesirable system voltage profile, or cause an increase in the fault level and the obstructions to fault detection, among other unwelcome consequences [2]. Furthermore, while those automated or computer-controlled loads in a modern electricity network require a high-quality power supply, there is still a significant portion of the loads that can accept a lower-quality supply but demand it to be supplied at lower prices. In this regard, an unbundled power-quality (PQ) supply is therefore a highly desirable feature for the distribution systems of the future, for it then enables the utility industry to deliver different PQ levels to match customer requirements and expectations [3]. For example, one could envisage a number of feeders emanating from a distribution substation and with each feeder providing a specific level of PQ. A wafer-fabrication manufacturing concern could be served by feeder(s) designed to deliver the highest PQ while domestic load customers are more likely to opt for a lower quality of supply served by different feeder(s). An appropriate tariff structure could then be designed which will have tariff rates commensurate with the quality levels.

In order to meet the above requirements, several conceptual power distribution systems have been proposed. The power-quality control center (PQCC) [4] is one such possible design, which could be particularly suited to supply an industrial/commercial park where customers in close proximity require varied quality in a power supply. Indeed, in [5], the authors describe the results of some preliminary works concerning a so-called premium PQ park. The park could be considered as a first step toward the development of a PQCC. In general, a PQCC attempts to overcome some of the technical challenges caused by the introduction of the DG while, at the same time, providing unbundled PQ services. The PQCC is located between the upstream transmission-distribution power system and the loads. Within the PQCC, power-electronic converters are used for increased flexibility in controlling power flows from the upstream system while the DG performs the duties of efficient generation of the balance of the energy needs and acts as the backup energy source, among other functions. In [4], [6], and [7], several possible designs of the PQCC and the corresponding operational schemes have been suggested. One common feature of the designs is that they offer some degrees of flexibility in terms of the operation of the distribution systems. Notwithstanding these interesting developments, one also notices that there are still a number of technical issues that warrant deeper investigation. For instance, as the DG would play a central role in the PQCC operation, the unavoidable interactions between the DG and the upstream system should be carefully examined. Moreover, the authors of [4], [6], and [7] have not considered the impacts of the upstream or downstream fault on the PQCC in terms of the manner by which the DG
and the power converters are controlled so as to maximize the potential benefits that can be extracted from the flexible distribution system.

Based on the work of [7], this paper presents an alternative PQCC scheme under both normal steady-state and upstream system fault conditions. The new scheme also strives for unbundled PQ supply by taking advantage of the flexibility offered by the PQCC, while certain operational constraints on the DG will be included. This is described in Section II. Section III describes the proposed PQCC operational scheme under steady-state as well as fault-contingency conditions. A brief description of the design of the control system for the PQCC system is then given in Section IV. Illustrative examples and simulation results are shown in Section V.

II. SOME BASIC CONSIDERATIONS

Among the various types of PQCC configurations described in [6] and [7], the uninterruptible power supply (UPS)-type PQCC is selected for further analysis and design in this paper due to its flexibility and simplicity. In this section, a brief description of the PQCC and the power-generating capability of the DG are presented.

A. UPS-Type PQCC Operational Scheme

The schematic diagram of the UPS-type PQCC as proposed in [7] is shown in Fig. 1. It consists of two pulse-width-modulated (PWM)-controlled inverters (Inv.1 and Inv.2) and a dc bus. Unlike a conventional UPS system, however, the PQCC is equipped with a generating source in the form of a DG instead of batteries on the dc bus. Under such an arrangement, continuous power generation is possible as long as fuel supply to the DG is available. The upstream power system is interconnected to the PQCC through two feeders fitted with hybrid transfer switches (HTSs). Each HTS consists of a pair of antiparalleled thyristors and a mechanical switch. Detailed configuration and operation of HTSSs can be found in [6]. As voltage transformation is envisaged between the upstream power system and the PQCC, a step-down transformer is shown connecting the upstream system and the point of common coupling (PCC) bus of the PQCC to reflect this requirement conceptually.

With this arrangement, there are three levels of PQ for the ac loads, namely ordinary quality (OQ), high quality (HQ), and super premium quality (SP), in addition to one quality level for dc loads. Under normal steady-state, the upstream system supplies the entire OQ and HQ loads, and part of the SP and dc loads through only one of the two feeders, called the primary feeder. Although the exact form of the DG has not been reported in [6] and [7], the DG is expected to meet the remaining SP and dc load demands.

Reference [7] proposes that should a fault occur on the primary feeder, its HTSs are turned off to isolate the fault from the PQCC and the HTSs on the standby feeder are turned on. The PQCC is then supplied by the standby feeder. Also, over the interval when the power transfer is switched from the primary to the standby feeders, the OQ load would be disconnected by the fast-acting circuit breaker (CB) C shown in Fig. 1. Any shortfall in meeting the HQ, SP, and dc load demands due to the delay in the switch-over transfer has to be met by the DG. In adopting this operating scheme, however, it is important to first consider the dynamic response capability of the DG. Depending on the relative sizes of the HQ, SP, and dc loads in comparison with the DG capacity, the DG output power would have to change rapidly and at the precise output level when the upstream power system transits from a healthy into a faulted state. In the reported work, it is not clear to what extent the DG can accommodate such a power change while maintaining the desired supply quality. Second, it is clear that this arrangement would only cater to faults that occur on one of the feeders. In practice, the number of fault incidents on the directly connected feeder will be negligible compared to that originating on the much more extensive upstream power system. Hence, it is doubtful whether the standby feeder-HTS scheme would be a cost-effective proposition in practice.

In view of the above, the present investigation re-examines the work reported in [6] and [7], with the view to propose a more realistic scheme for the UPS-type PQCC under both steady-state and system fault conditions.

B. Solid-Oxide Fuel-Cell-Distributed Generator

In recent years, fuel cells (FCs) have emerged as attractive power sources since they are energy efficient, friendly to the environment and can provide reliable service [8], [9]. While there are various kinds of FCs, the high-temperature solid-oxide fuel-cell (SOFC) power plant has been chosen as the form of DG in the present work. The main reason for doing so is because of the impressive service record of a number of demonstration units of SOFC-DG currently in operation. SOFC-DG can make use of hydrogen-rich natural gas as fuel instead. It converts the chemical energy directly into dc electrical energy [10]. Although the energy conversion process is highly complex for the purpose of the present investigation, it would be sufficient to consider the schematic diagram of the SOFC-DG shown in Fig. 2 [11], [12]. Two main parts of the DG can be readily identified. The part called the balance of plant (BOP) consists of the natural gas fuel storage, fuel valve, and the fuel processor. The fuel processor reforms the natural gas, of which input flow rate \( N_{f} \), to the hydrogen-rich fuel with a flow rate of \( N_{H_2}^{\text{in}} \). The processor dynamic can be approximated by a first-order lag model of time constant \( \tau_f \) [12]. The part labeled as the FC stack is where the complex thermodynamic and electrochemical processes take place. Details are given in [10].

On the electrical characteristics, the SOFC terminal voltage \(V_{\text{th}}\) can be expressed as

\[
V_{\text{th}} = E - r_{f\text{FC}} \quad (1)
\]
where \( E \) is the internal electromotive force (emf) and \( I_{FC} \) is the FC stack current [12]. Within the stack, there are the so-called ohmic, activation, and concentration losses. In the model, \( r \) accounts for the losses. The calculation of \( E \) is through the application of the well-known Nernst equation [10]. The expression is a highly nonlinear function of the hydrogen, oxygen, and water partial pressures and cells operating temperature. The electrochemical process to generate \( E \) is shown simply in Fig. 2 by the block “Nernst and thermodynamics equations” where the input variables are \( N_{H_2}^m \) and \( J_{FC} \). \( E \), therefore, depends essentially on the SOFCDCG internal electrochemical dynamics. As can be seen in [11] and [12], a change in the stack current \( I_{FC} \) will typically take tens of seconds for its effect to be fully felt in \( E \). In the context of the problem in hand, however, one of the main considerations in the design of the PQCC is to mitigate the effects of upstream voltage-sag event on loads [7]. Often, the voltage sag is caused by the occurrence of a short-circuit fault in the upstream system. The duration of the sag is typically of the order of 1 s or less. Hence, \( E \) can be assumed to be essentially constant under such a short-duration disturbance condition and (1) can be used to describe the circuit behavior of the DG. Furthermore, the SOFCDCG output power \( (P_{dc}) \) is then given by

\[
P_{dc} = V_{dc} \cdot I_{FC}.
\]  

(2)

Next, the operation of SOFCDCG has to take into account two practical constraints. The first pertains to the fuel utilization factor \( u \). It is a most important operating variable affecting the performance of SOFCDCG and is defined as

\[
u = \frac{N_{H_2}^m - N_{H_2}^f}{N_{H_2}^m}
\]

where \( N_{H_2}^m \) and \( N_{H_2}^f \) are the input and output flow rates of hydrogen in the FC stack. It is shown in [12] that \( u \) can also be expressed in terms of \( I_{FC} \) as

\[
u = \frac{2K_JI_{FC}}{N_{H_2}^m}
\]

(3)

where \( K_J \) is a modeling parameter. The typical desirable range of \( u \), expressed as \( u_{\text{min}} \leq u \leq u_{\text{max}} \), is from 0.7 to 0.9. The overused-fuel condition \( (u > 0.9) \) could lead to permanent damage to the cells due to fuel starvation whereas the underused-fuel situation \( (u < 0.7) \) results in undesirably high cell voltages.

The second constraint concerns the FC output power \( P_{dc} \). It should be maintained within the range

\[P_{\text{min}} \leq P_{dc} \leq P_{\text{max}}.
\]

(4)

The minimum power constraint is necessary in order to maintain stable stack operating temperature and acceptable power plant efficiency [13]. \( P_{\text{max}} \) denotes the design power rating of the SOFCDCG.

Due to the above considerations, Fig. 3 shows the steady-state feasible operating area (FOA) of the SOFCDCG derived using the typical data given in [12]. The operating temperature of the FC stack is assumed constant in deriving the FOA, and the detailed construction of the FOA has been described in [11]. In arriving at this diagram, the FC-rated power and output voltage are used as the base quantities. The constraints placed on \( u \) are represented by the horizontal lines AB and BC. The limits on \( P_{dc} \) are shown by the curves AB CD. The FOA of the SOFCDCG would correspond to the area “ABCD.” The SOFCDCG must operate within the FOA as an operation that is outside FOA will reduce cell life and is unacceptable. Also shown in Fig. 3 is the curve \( \nu = Y \) describing the operational characteristic of the SOFCDCG under a constant \( V_{dc} \), condition. It shows \( V_{dc} \), operating within the voltage limits \( V_{\text{dc.min}} \) and \( V_{\text{dc.max}} \).

According to (3), if one were to operate the FC stack with a fuel input \( (N_{H_2}^m) \) proportional to \( I_{FC} \), as governed by

\[
u = \frac{2K_JI_{FC}}{N_{H_2}^m}
\]

it will result in a constant utilization factor \( u \), under steady-state. Such an input fuel-control scheme has been incorporated in Fig. 2. Notice that by adopting a constant \( u \) operational scheme, such as that described by the line EF in Fig. 3, \( V_{dc} \) would vary as \( I_{FC} \) changes.

In order to ensure that cell operation will remain within the FOA during power system disturbances, one needs to determine the maximum step change in \( I_{FC} \) so that the SOFCDCG can accommodate. Based on (3), a step change of \( I_{FC} \) can be related to a step change in \( u \) as

\[
\Delta I_{FC} = \frac{N_{\text{H}_2}^m}{2K_J} \cdot \Delta u
\]

(6)
where the hydrogen input $N_{H_2}^{in}$ will remain constant following the sudden step disturbance. This is due to the time lag in the fuel processor. In what follows, the subscript “0” would be used to denote the initial value of the respective variable. Hence, the initial operating current and utilization factor are $I_{FC,0}$ and $u_0$, respectively. It can be deduced that

$$\frac{\Delta I_{FC}}{I_{FC,0}} = \frac{\Delta u}{u_0}.$$  \hspace{1cm} (7)

Since the acceptable range of $u$ is from $u_{min}$ to $u_{max}$, one can define a new variable $z$, where

$$z = \begin{cases} \frac{(u_{max} - u)}{u} & \text{for positive } I_{FC} \text{ step} \\ \frac{(u - u_{min})}{u} & \text{for negative } I_{FC} \text{ step} \end{cases}$$  \hspace{1cm} (8)

Therefore, the maximum step change in $I_{FC}$, which can be accommodated by the SOFC/DG is given by

$$\Delta I_{FC} \leq |z| I_{FC,0}.$$  \hspace{1cm} (9)

By neglecting the small voltage drop due to the ohmic loss in (1) and then using (2), the maximum step change in the SOFC/DG power output while constrained by the FOA can be written as

$$\Delta P_{dc,0} \leq |z| P_{dc,0}.$$  \hspace{1cm} (10)

where $P_{dc,0} = V_{dc,0}I_{FC,0}$ is the initial operating power level.

For a power change larger than that given by (10), however, the SOFC/DG would require several tens of seconds for the fuel processor to adjust the fuel input [12] if the cell is to be constrained to operate within the FOA.

In summary, it is clear that there is a practical limit in terms of the magnitude and the speed by which the DG can respond to a power change demand. Due to the inherent slow dynamics of the SOFC/DG, a buffer energy storage device, such as a supercapacitor or battery, is often used in conjunction with the SOFC/DG system [14], [15]. In the next section, however, an alternative scheme is proposed in which the buffer energy storage device would not be included. Instead, the intention is to investigate to what extent the PQCC can meet the need of providing an unbundled PQ supply for loads.

III. PROPOSED SCHEME

Recognizing the shortcomings and practical problems faced in implementing the PQCC scheme described in [7], a new scheme is now proposed. The design is based on the consideration of normal system conditions as well as pertaining to the upstream faults or outages of the various components in the power system.

A. Steady-State Operation Under Normal Conditions

To facilitate the description of the scheme, the simplified equivalent circuit PQCC model shown in Fig. 4 is used. The standby feeder and its HTS used in [7] have been excluded in the present proposal. The upstream system is represented simply as an equivalent voltage source with its equivalent series impedance $R + j\omega L$, where $\omega$ is the ac system frequency. In this figure, the ac voltages are measured phase-ground whereas the power flows are three-phase quantities. The PCC voltage phasor is treated as the reference quantity and is denoted as $V_p$. $\bar{V}_p$ is assumed measurable. The SP, dc, HQ, and PQ loads are represented by the respective impedances for convenience although in the analysis to follow, the load model used is a more general static-type. $I_f$ represents the total inductance which links the PCC to Inv.1. It also acts as a filter for high-frequency harmonics produced by the inverter and, therefore, the inverter harmonics are ignored in the following analysis. Although the SOFC/DG in Fig. 4 is shown as a dc source, its functional characteristics described in Section II-B are included in the analysis. The capacitor $C_f$ shown connected to the dc bus is only used for filtering purposes and is not sufficiently large to be an effective energy storage device.

Within Inv.1, the two control variables are the modulation index $m$ and the phase angle $\theta_f$ of the inverter. By controlling them, Inv.1 front-end voltage $V_f$ as shown in Fig. 4 can be regulated and the power flow $P_f + jQ_f$ is, therefore, controlled. The role of Inv.2 is to ensure a high ac waveform quality at the terminals of the SP load, again through the control of its modulation index and phase for a given $V_m$.

Reference [7] proposed that the DG supply part of the dc and SP load under steady-state. Notice that in the event of a decrease of upstream infeed at point “A,” the SOFC/DG has to increase its output power instantaneously to satisfy the load demand. However, (10) shows that the SOFC/DG has only a limited ability to react instantaneously to a real power change. Hence, the scheme in [7] has to be modified, as follows.

In the scheme proposed here, the SOFC/DG supplies the entire power demand of dc and SP under the normal steady-state. As the dc and SP load demands vary, the output power of the SOFC/DG would vary correspondingly. This load-tracking feature of the SOFC/DG is possible, as the expected load variations would be slow compared to the response of the SOFC/DG power plant. Clearly, the rating of the SOFC/DG will have to be at least that of the maximum of the total dc and SP load demands. This is a reasonable requirement because in the event of a complete loss of the upstream system, the SOFC/DG will then be called upon to meet the total demand of the dc and SP loads.

Having met the total dc and SP load demands, the SOFC/DG could also inject any surplus active power ($P_f$) to the PCC bus. $P_f$ is then used to meet part of the OQ and HQ load demands.
Under such an arrangement, the SOFC DG and downstream loads are always connected to the upstream grid, by having Inv.1 operating in the inversion mode. The upstream power system therefore acts as a backup source for the SP and dc loads in the event of the loss of the SOFC DG. Hence, the SP and dc loads enjoy a supreme level of supply reliability as intended.

While the above operating scenario can be expected to prevail under almost all instances, one has to consider the possibility of the loss of the connection between Inv.1 and the PCC (i.e., “B” open). This is a necessary consideration because this incident will result in the output of the SOFC DG being decreased suddenly by an amount equal to $P_r$. In order to safeguard the FC stack, the steady-state predisturbance value of $P_r$ (i.e., $P_{r,0}$) must therefore be constrained to within the range

$$0 \leq P_{r,0} \leq |e| P_{d,0}.$$  \hspace{1cm} (11)

Equation (11) is obtained as a consequence of (10) so as to satisfy the FOA constraint. However

$$P_{d,0} = P_{s,d} + P_{r,0}$$  \hspace{1cm} (12)

where $P_{s,d}$ is the total power demand of the SP and dc loads. The output power of SOFC DG $P_{d,0}$ must also satisfy the output-power limit constraint (4) during steady-state operation. Combining the requirements (11) and (12), one arrives at the condition

$$[1 - |e|] P_{d,0} \leq P_{s,d} \leq P_{d,0}.$$  \hspace{1cm} (13)

Yet another steady-state consideration concerns the possibility of reverse power flow into the upstream power system. In the event that the total demand of the HQ and OQ loads is less than $P_{d,0}$, the surplus portion of $P_{d,0}$ would flow reversely back to the upstream system. In case of contractual reasons, assume such reverse power flow is prohibited. Therefore, it is proposed that active power flow in the upstream feeder (point “A”) is continuously monitored. Once reverse power flow is detected, the output power ($P_{d,0}$) of the SOFC DG is to be reduced until the reverse power flow ceases and (13) is also satisfied.

Apart from supplying active power to the HQ and OQ loads, it is also proposed that Inv.1 is set to supply all of the reactive power required by the OQ and HQ loads under steady-state, that is

$$Q_{r,0} = Q_{o,q,0} + Q_{h,q,0}.$$  \hspace{1cm} (14)

Power factor at “A” is therefore maintained at unity. The voltage drop across the equivalent system impedance ($R + jX_L$) is therefore minimized. This is desirable as it will ensure sufficient power transfer through the upstream system.

Finally, the current rating of Inv.1, denoted as $I_{inv1,rate1}$, needs to be examined. Under this normal steady-state condition, Inv.1 must be rated to supply the proportion of the total maximum HQ and OQ load demands. Thus, by expressing power as the total three-phase quantity and voltage as phase-ground value

$$I_{inv1,rate1} = \sqrt{\frac{(P_{d,0}^2 + Q_{r,0}^2)}{3 V_{d,0}^2}}.$$  \hspace{1cm} (15)

B. Operation Under the Loss of SOFC DG

Next, consider the loss of all or part of the SOFC DG output power. This incident could be due to an internal fault or malfunction of the FC system. In order to protect the dc and SP loads from the impact of the loss of $P_{d,0}$, the upstream system must be forced to supply the additional active power as rapidly as possible to meet the downstream load demands. Therefore, Inv.1 is to operate in rectifier mode to transfer active power from the upstream system to the dc bus.

The most severe condition under this contingency scenario would be the total loss of power from the SOFC DG. The upstream system must have sufficient capacity to supply the maximum dc and SP total real power demand ($P_{s,d, max}$). Hence, one arrives at another Inv.1 current rating

$$I_{inv1,rate2} = \frac{P_{s,d, max}}{3 V_{d,0}^2}.$$  \hspace{1cm} (16)

Combined with (15), the current rating of Inv.1 is therefore the larger value of $I_{inv1,rate1}$ and $I_{inv1,rate2}$.

C. Operation Under Upstream Voltage Sag

A most devastating voltage disturbance on loads is that of a voltage sag. Due to the complexity and extensiveness of the upstream system, only the balanced voltage sag will be considered and the sag magnitude is assumed constant over the duration of the disturbance. The SP and dc loads could be shielded from the effects of the decrease in the PCC voltage by the corrective actions of Inv.2. However, as the OQ and HQ loads are connected directly to the PCC, they will experience the full impact of the sag. Thus, special consideration must be given to improve the ride-through capability of the HQ and OQ loads during the voltage disturbance.

Electrical equipment manufacturers often provide information describing the withstand voltage versus time characteristics of their products. A well-known and typical example is the ITI (CBEMA) curve. It relates the duration that a load is capable of withstanding a depressed voltage of a given magnitude. Hence, the more severe a sag is, the shorter the tolerable duration $T$. The curve also indicates the lowest acceptable voltage level $V_{min}$ for continuous operation of the loads. Suppose the HQ and OQ loads have such voltage/time characteristics. The proposed ride-through scheme described below will then depend on which of the two scenarios the sag is related to.

Scenario 1: This scenario pertains to sags that have a duration of less than the specified $T$ for the HQ and OQ loads. Under such a situation, the HQ and OQ loads would not be damaged and they can still be connected to the upstream system. Thus, the system continues to be operated as under the normal steady-state.

Scenario 2: If the sag duration were to exceed $T$, it is proposed that the switch C connected to the OQ load would operate
at the first available instance to disconnect the OQ load from the network. The OQ load only requires the lowest level of supply quality among all of the loads. Thus, it is chosen to be the first to be disrupted due to the severe sag. After the shedding of the OQ load, if the PCC voltage recovers to above \( V_{\text{min}} \), the PQCC would revert to the Scenario 1 control scheme described earlier but without the OQ load. Otherwise, the switch "A" in Fig. 4 in the upstream interconnection link is opened at the earliest possible instant. The PQCC system then operates in an islanding mode.

Although the OQ load can be shed, the HQ load would still be supplied by the PQCC under this islanding stage. The initial power supplied by the SOFCDG to the upstream load \( P_{\text{r,0}} \) is unlikely to match the HQ load requirement exactly. Thus, the output power of the SOFCDG has to be regulated subsequent to the islanding. From (10), it is shown that the instantaneous power change imposed on the SOFCDG must be kept at least less than \( \epsilon \mid P_{\text{d},0} \). While Inv.1 still operates in inversion mode, it is proposed that its role is changed from controlling \( P_r + f Q_r \) to controlling the PCC bus voltage \( (V_p) \) and in maintaining the bus frequency. The objective is to maintain \( V_p \) at \( V_{\text{min}} \). Hence, following the islanding, the HQ load will draw power \( P_{\text{H(Q)},\text{iso}} \) at a level that should be limited to

\[
0 \leq P_{\text{H(Q),iso}} \leq P_{\text{r,0}} + \epsilon \mid P_{\text{d},0}. \tag{17}
\]

Combining (17) and (11), the possible power range of the HQ load in the islanding mode can be determined as

\[
0 \leq P_{\text{H(Q),iso}} \leq 2 \mid \epsilon \mid P_{\text{d},0}. \tag{18}
\]

However, as the initial operating condition is governed by (13), it can be combined with (18) to yield

\[
0 \leq P_{\text{H(Q),iso}} \leq \left( \frac{2 \mid \epsilon \mid}{1 - \mid \epsilon \mid} \right) P_{\text{SP}}. \tag{19}
\]

One must also limit the power output of the DG to the range specified by (4), that is

\[
P_{\text{min}} \leq P_{\text{SP}} + P_{\text{H(Q),iso}} \leq P_{\text{max}}. \tag{20}
\]

The HQ load in the isolated mode as obtained from Fig. 5 can then be translated to its prefault value by applying

\[
P_{\text{H(Q),0}} = \frac{P_{\text{H(Q),iso}}}{V_{\text{min}}} \tag{22}
\]

where \( P_{\text{H(Q),0}} \) and \( P_{\text{H(Q),iso}} \) denote the prefault and postfault HQ load power demands, respectively, and \( \alpha \) is the real power-voltage index [16] of the HQ load. Typically, \( 0 \leq \alpha \leq 2 \).

Based on the above consideration, it can be seen that the loads are to be served by a power-supply system with varied quality levels. Table I summarizes the levels enjoyed by the OQ, HQ, SP, and DC loads under the proposed arrangement.

### Table I

<table>
<thead>
<tr>
<th>Loads</th>
<th>Waveform quality</th>
<th>Voltage sag ride-through attribute</th>
</tr>
</thead>
<tbody>
<tr>
<td>OQ</td>
<td>Not improved</td>
<td>Low</td>
</tr>
<tr>
<td>HQ</td>
<td>Not improved</td>
<td>High</td>
</tr>
<tr>
<td>SP</td>
<td>Very high</td>
<td>Very high</td>
</tr>
<tr>
<td>DC</td>
<td>Improved</td>
<td>Very high</td>
</tr>
</tbody>
</table>

IV. CONTROL SCHEME

The control scheme for the varied PQCC operating conditions is considered next. The interconnected system, which includes the normal steady-state, SOFCDG outage state, and upstream voltage sag (Scenario 1), will be described first.

A. Control Scheme Under Interconnected State

From Fig. 4, the voltage-current relationship of the ac link between the PCC and Inv.1 in the \( d-q \) frame can be written as

\[
\frac{d}{dt} \begin{bmatrix} i_d \\ i_q \end{bmatrix} = \begin{bmatrix} 0 & \omega L_f \\ -\omega L_f & 0 \end{bmatrix} \begin{bmatrix} i_d \\ i_q \end{bmatrix} + \begin{bmatrix} V_{f,q} - \sqrt{3}V_p \\ V_{f,q} \end{bmatrix} \tag{23}
\]

where \( [i_d, i_q]^T \) and \( [V_{f,d}, V_{f,q}]^T \) contain the \( d-q \) components of the vectors of the ac-link phase currents \( i_f \) and Inv.1 front-end phase voltages \( V_f \), respectively. It is seen from (23) that cross-coupling effects exist between the state variables \( i_d, i_q \) and the control variables \( \alpha, \phi_f \). A decoupled control scheme based on [17] was used in this investigation to obtain Fig. 6, in which \( i_d \) and \( i_q \) are seen to track the reference.
Fig. 6. Control scheme of Inv.1 under interconnected state.

currents $i^d_P$ and $i^q_P$ are obtained through the proportional-integral controller $G(s)$. The regulated signals ($\Delta V_P^d$ and $\Delta V_P^q$) are then added to the initial voltages ($V_{P,0}$ and $V_{Q,0}$) to produce the reference voltage signals ($V_{P}^d$ and $V_{Q}^q$). These reference voltages are used to generate the control variables of Inv.1. In designing $G(s)$, the frequency-response tuning technique has been adopted. As the switching actions of Inv.1 may affect the dynamic performance of the PQCC, it is a good practice to set the bandwidth of the closed-loop system to be from one-fifth to one-half of the switching frequency.

B. Control Scheme Under Islanding Mode (Scenario 2)

Under Scenario 2, the PQCC is disconnected from the upstream power system. Inv.1 is to maintain $V_{P}$ at $V_{rated}$ at the rated frequency. Under this islanding mode, the phase angle $\delta$ is no longer a control variable as the isolated system has only one voltage source connected to the ac link. The control scheme for Inv.1 is therefore similar to that of Inv.2, details of which are well known (see, for example, [18]).

C. Determination of Current References

Having examined the controller design, the current references for the different operation states will now be given.

Under Normal State: Under steady-state, Inv.1 transfers active power $P_{dc}$ from the dc bus to the ac system and also supplies all of the reactive power required by the HQ and QO load. Therefore, the current references are given by

$$i^d_P = \frac{P_{dc}}{(3W_{p,r})} \quad i^q = \frac{(Q_{Q,0} + Q_{HQ,0})}{(3W_{q,r})} \quad (24)$$

Under Sudden Loss of SOFCDCG: Under the SOFCDCG outaged condition, Inv.1 changes from inverter mode to rectifier mode. Inv.1 carries the total active power demand of the dc and SP loads, with $Q_r = 0$. Thus

$$i^d_P = \frac{P_{dc}}{(3W_{p,r})} \quad i^q = 0 \quad (25)$$

Under Voltage Sag (Scenario 1): Under this scenario, suppose the PCC voltage reduces to $V_{P,\text{sag}}$. Inv.1 has the same amount of active power ($P_{dc}$) as before the sag from the dc bus to the ac system while, at the same time, providing all of the reactive power needs of the QO and HQ loads. Therefore

$$i^d_P = \frac{P_{dc}}{(3W_{p,\text{sag}})} \quad i^q = \frac{(Q_{Q,\text{sag}} + Q_{HQ,\text{sag}})}{(3W_{q,\text{sag}})} \quad (26)$$

Note that in generating the current references, the PQCC loads would vary as part of the normal operating condition. Hence, $i^d_P$ and $i^q$ will have to be adjusted continuously to track the load changes.

V. ILLUSTRATIVE EXAMPLES

A. System Description

In order to ascertain the effectiveness of the proposed operating scheme, a series of simulation studies have been performed. The example system used in the studies is as shown in Fig. 4, with the corresponding system data given in the Appendix (Table II). The SOFCDCG dynamic model shown in Fig. 2 is incorporated into the PQCC in the simulation in order to improve the accuracy of the study. The SOFCDCG rating is $P_{max} = 100 \text{ kW}$ and the relevant parameters used in the simulation were obtained from [12]. The fuel utilization factor under steady-state is assumed to be 0.8, with $n_{min} = 0.7$ and $n_{max} = 0.9$. From (8) and (10), it is then established that $\Delta P_{Q,min} < 0.125 P_{Q,p}$.

The HQ and QO loads are assumed to be of constant impedance type (i.e., $\alpha = 2$). Their withstand voltage-time characteristic is assumed to be governed by the ITI curve with $V_{min} = 0.9 \text{ p.u.}$. Therefore, the maximum HQ load power that can be supported by the PQCC under islanding mode at the minimum voltage can be calculated from (21), viz. $P_{HQ,Q,min,max} = 22.22 \text{ kW}$. From (22), this corresponds to a maximum fault power of $P_{HQ,Qmin,max} = 27.13 \text{ kW}$. Therefore, in the illustrative example, the initial HQ load power demand could be chosen up to 27.43 kW.

System and load parameters are then calculated according to the following assumed active power distribution: under initial steady-state $P_{dc,0} = 90 \text{ kW}$, $P_{PC} = 30 \text{ kW}$, $P_{SP} = 50 \text{ kW}$, $P_{Q} = 10 \text{ kW}$, $P_{Q,HQ} = 175 \text{ kW}$, and $P_{Q,SP} = 25 \text{ kW}$. The power factor of the HQ and QO loads is considered as 0.95 lagging. Inv.1 is represented by six on-off switches with small finite series resistances. The switching frequency of Inv.1 is 4 kHz.

Inv.1 current rating can be determined using the approach described in Section III. In the example, it is seen that $I_{inv,1 rated} - I_{inv,1 rated} = 30\%$. Inv.1 controller bandwidth is selected to be one-quarter of the switching frequency. The corresponding PI controllers for Inv.1 when the PQCC is operating under interconnected condition has been determined as $G(s) = 0.05 \times 0.05/s$.

B. Simulation

Simulation studies have been carried out using Matlab/Simulink to verify the effectiveness of the proposed scheme. Results of four cases are described: that of voltage sag...
to 90% and 60% of its nominal value, under the sudden loss of the SOFC and under the sudden loss of the ac link (CB "B" open). For the SOFC, as both the hydrogen input and stack current can be measured on line, \( n_u \) can therefore be determined in real time using (3). The corresponding simulation results are shown in Figs. 7–10.

Fig. 7 shows the system response under the 90% sag condition. Since the HQ and OQ loads are assumed capable of sustained operation at this voltage level, the PQCC system would therefore remain connected to the upstream power system during the sag. Inv.1 transfers an almost constant 10 kW throughout the duration of the disturbance. This can be seen in Fig. 7(d). However, due to the voltage dependency of the reactive power demand of HQ and OQ loads, \( Q_s \) supplied by the PQCC is reduced as shown in Fig. 7(d). As the output power of SOFC is unchanged, Fig. 7(a) and (b) confirm that the FC fuel utilization factor and the dc-link voltage are undisturbed during the sag. As the real power demand of the HQ and OQ loads decreases, the power drawn from the upstream power system reduces as shown in Fig. 7(c) while the reactive power from this source remains at zero since the PQCC supplies all of the reactive power required by these loads.

Fig. 8 shows the consequence of a voltage sag (60%) for which the PQCC needs to operate in the islanding mode. From the ITI curve, the maximum duration (\( T \)) of the HQ and OQ loads that could tolerate the 60% sag is about 20 ms. Therefore, in Fig. 8, the OQ load is assumed successfully disconnected by the action of the CB 10 ms after the inception of the sag. Since the PCC voltage (\( V_{PCC} \)) remains below \( V_{lim} \) after the load shed, the PQCC system is then disconnected from the upstream power system half a cycle later. Thereafter, \( P_{sy} = Q_{sy} = 0 \) as shown in Fig. 8(c). The operating mode of Inv.1 is changed from controlling the power flow to regulating \( V_{PCC} \) and frequency. Fig. 8(a) shows that \( V_{PCC} \) can indeed be restored to 0.9 p.u. during this islanding mode operation. The SOFC output power increases by 10 kW as evident from Fig. 8(d) and the step power change is within the FOA of the SOFC. The SOFC fuel utility factor \( n_u \) increases but does not exceed \( n_{max} \). The dc-link voltage decreases slightly following the islanding stage.

Fig. 9 shows the system response under the sudden loss of the SOFC. In order to protect the dc and SP loads, the upstream system has to meet all of the active power load demand rapidly and Inv.1 is therefore changed from inversion to rectification operating mode. It is seen that \( V_{PCC} \) decreases at the instance of the DG disconnection. However, it recovers rapidly because of the support from the upstream system. The loads are almost unaffected. Inv.1 current would increase to its rated value during the fault, as can be seen from Fig. 9(b).

The simulation results under the sudden loss of the ac link (CB "B" open) are given in Fig. 10. Both real and reactive powers supplied by the upstream system shown in Fig. 10(a) increase as the loads are totally supplied by the upstream system. Since \( P_{sy} \) was chosen to satisfy (11), the sudden power change would not damage the SOFC, as can be seen from Fig. 10(b). The utilization factor \( n_u \) is within its limit during the fault event.

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VI. Conclusion

The operation scheme of the PQCC under steady-state, upstream voltage sag, or ac interconnection outage states has been presented. The DG considered is in the form of an SOFC. Unlike previous works on the PQCC, the role of the DG and the inverters in the PQCC are examined in regards to mitigating the impacts of the fault disturbances on loads. By taking into consideration the ability of the SOFCDG to track load changes, it is proposed that the SOFCDG supplies all of the downstream loads under steady-state. Upstream voltage sag is dealt with in accordance to the severity of the sag. Two possible scenarios have been considered in which the design criteria provide maximum support for load ride-through while maintaining the unbalanced power-supply feature of the PQCC. The proposed scheme exploits more fully the control of the inverters and the SOFCDG to mitigate the voltage-sag effect rather than rely on the use of alternative feeders and the transfer switches, as was suggested in earlier works. Analysis of the power system response and the design of the control system of the PQCC have been also presented. The efficacy of the scheme is born out by simulation results.

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[20] Y. H. Li received the B.E. degree from Xi’an Jiaotong University, Xi’an, China, in 2004. He is currently pursuing the Ph.D. degree with Nanyang Technological University, Singapore.

Y. Q. Zhan received the B.E. degree from Xi’an Jiaotong University, Xi’an, China, in 2004. He is currently pursuing the Ph.D. degree with Nanyang Technological University, Singapore.

S. S. Choi received the B.E. and Ph.D. degrees from the University of Canterbury, Christchurch, New Zealand, in 1973 and 1976, respectively. He was with the New Zealand Electricity Department, Wellington, National University of Singapore, and the State Energy Commission of Western Australia, Perth. Currently, he is a Professor in the School of Electrical and Electronic Engineering, Nanyang Technological University, Singapore.

S. Rajakumar received the B.Sc.Eng. degree from the University of Moratuwa, Sri Lanka, in 1986, the M.Sc. degree from the University of Calgary, Calgary, AB, Canada, in 1989, and the Ph.D. degree from the University of Toronto, Toronto, ON, Canada, in 1993. He was a Senior Lecturer with the Department of Electrical Engineering at the University of Moratuwa from 1993 to 1999. Currently, he is an Associate Professor in the School of Electrical and Electronic Engineering, Nanyang Technological University, Singapore. His research interests include distributed generation, energy storage technologies, and induction generators.

D. Mahinda Vilathgamuwa received the B.Sc. degree in electrical engineering from the University of Moratuwa, Sri Lanka, in 1985, and the Ph.D. degree from Cambridge University, Cambridge, U.K., in 1993. Currently, he is an Associate Professor in the School of Electrical and Electronic Engineering, Nanyang Technological University, Singapore. His research interests are in power quality, control of power converters, and electric drives.